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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington,

D.C. 20231 on_January 31, 2002.

Rosalie Ann Centeno, Secretary

In the application of:

Wilfred Lerch et al

Serial Number:

09/980,754

Filing Date:

November 2, 2001

For:

Method of Generating Defects in a Lattice Structure of a Semiconductor

Assistant Commissioner for Patents

Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

In accordance with 37 CFR § 1.56, Applicant wishes to call the attention of the Examiner to the following reference(s):

- 1) XP-002101565
- 2) WO 99/01895
- 3) XP-000669354
- XP-000974859
- WO 99/39381

References 1 - 5 have been cited in the International Search Report and are submitted to provide the Examiner easy access to said references.

Copies of the listed documents are submitted herewith along with the form PTO-1449.

Consideration of the foregoing in relation to this application is respectfully requested.

Respectfully submitted,

Robert W. Becker, Reg. No. 26,2

for the Applicant

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Enclosures: PTO 1449 / References - 5